

Synopsys and Open-Silicon

High Quality IP Saves Three Months on Schedule

“In selecting an IP supplier we look at quality, features, and support. Synopsys came out on top in all three areas by providing production-proven and certified IP with the required set of features. We also appreciated the responsiveness of the expert support team, who was there for us every step of the way.”

– Hans Bouwmeester, Director of IP at Open-Silicon



Overview

Open-Silicon, a full service ASIC company delivers cost-effective and reliable customer ASIC solutions to the consumer electronics market. Open-Silicon optimizes the chip supply chain through fabrication process technologies, pre-qualified IP, package assembly and test solutions, and a world-class design team. To help build their customer's next-generation 90-nanometer networking chip, Open-Silicon selected Synopsys as the “go-to” vendor for their IP needs.

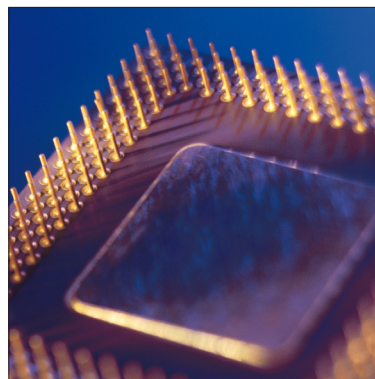
Shorter time-to-market, reduced costs, and decreased engineering effort were important factors for Open-Silicon when designing their latest networking chip. They relied on Synopsys' leading market position in PCI Express® and USB IP to help achieve these factors and meet their market window. Synopsys provided Open-Silicon with access to high performance, proven IP that was up to 15% lower in area and power than other offerings. In addition, the detailed documentation helped the engineers get up to speed on the IP in a matter of days. With production proven and certified IP, Open-Silicon felt assured that the IP would interoperate successfully in the customer's end product.

Synopsys DesignWare IP Solution

- Hi-Speed USB 2.0 OTG Controller, PHY & Verification IP
- PCI Express Controller & PHY
- I/O Libraries

Cooperation Benefits

Open-Silicon's design and manufacturing expertise combined with Synopsys' DesignWare® IP, enabled first-pass silicon success. The result was a design that exceeded their expectations in area, power and ease of integration. With DesignWare IP, Open-Silicon was able to configure, integrate and simulate the IP in days saving almost three months on their schedule.



SYNOPSYS®
Predictable Success

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